

**Unit I**

1. Explain with examples booth multiplication algorithm. 15
2. Design two bit comparator. 15

**Unit II**

3. Explain analog to digital convertor. 15
4. Explain general register organization. 15

**Unit III**

5. Explain instruction set based classification of processor. 15
6. What is instruction cycle ? Explain. 15

**Unit IV**

7. Explain the following : 15
  - (a) Handshaking modes
  - (b) DMA.
8. Explain the serial versus parallel data transmission. 15

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**No. of Printed Pages : 02**

**Roll No. ....**

**BB-684**

**M.C.A. EXAMINATION, May 2017**

(Second Semester)

(B. Scheme) (Main & Re-appear)

**COMPUTER ORGANIZATION AND ARCHITECTURE**

MCA-406

*Time : 3 Hours]*

*[Maximum Marks : 75*

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Before answering the question-paper candidates should ensure that they have been supplied to correct and complete question-paper. No complaint, in this regard, will be entertained after the examination.

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**Note :** Attempt *Five* questions in all, selecting at least *one* question from each Unit. All questions carry equal marks.

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**P.T.O.**