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Roll No. ....

**405**

**B. Tech. EXAMINATION, May 2017**

(Fourth Semester)

(Old Scheme) (Re-appear Only)

(CSE)

CSE-210

COMPUTER ARCHITECTURE AND  
ORGANIZATION

*Time : 3 Hours]*

*[Maximum Marks : 100*

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Before answering the question-paper candidates should ensure that they have been supplied to correct and complete question-paper. No complaint, in this regard, will be entertained after the examination.

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**Note :** Attempt any *Five* questions. All questions carry equal marks.

(3-24/1) M-405

**P.T.O.**

1. (a) Explain difference between combinational and sequential logic Circuit. **10**  
 (b) Design a 3 bit synchronous counter. **10**
2. (a) Explain Flynn's classification of computers. **10**  
 (b) Explain Cache, Main Memory and Secondary Memory. **10**
3. Explain types of addressing modes with example. **20**
4. (a) Explain instruction set formats. **10**  
 (b) Explain, how performance are enhancing with pipelining ? **10**
5. (a) What is locality of references ? Explain Memory Hierarchy. **10**  
 (b) Explain direct mapped organization of cache memory. **10**
6. Explain instruction level and processor level parallelism. **20**

7. (a) Explain types of instructions set. **10**  
 (b) Explain types of interrupts. **10**
8. (a) What are the different types of CPU architecture ? **10**  
 (b) Explain pipelining. Also explain its advantages. **10**