

703

B. Tech. EXAMINATION, 2020

(Seventh Semester)

(Old Scheme) (Re-appear Only)

(CSE)

CSE405

ADVANCED COMPUTER ARCHITECTURE

Time : 2½ Hours

[Maximum Marks : 100

Before answering the question-paper candidates should ensure that they have been supplied to correct and complete question-paper. No complaint, in this regard, will be entertained after the examination.

Note : Attempt *Four* questions in all. All questions carry equal marks.

1. Explain various Cache Coherence models.
2. What are different types of clocking used during data transfer among registers ? Also explain various types of registers.
3. Explain the economics of a processor project. Also explain evaluation matrix.
4. What is Associative Memory ? Derive match logic for one word of associative memory ?
5. Explain processor memory modelling using queuing theory.
6. Explain the following :
 - (i) Physical Memory
 - (ii) Mixed Queue Model.

7. What are concurrent processes ? Explain vector processes.
8. Explain the following :
 - (a) Orthogonal Instruction Set
 - (b) Virtual address to real address translation.